



FSMTC 18-749

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Approved
TP
1/20/03

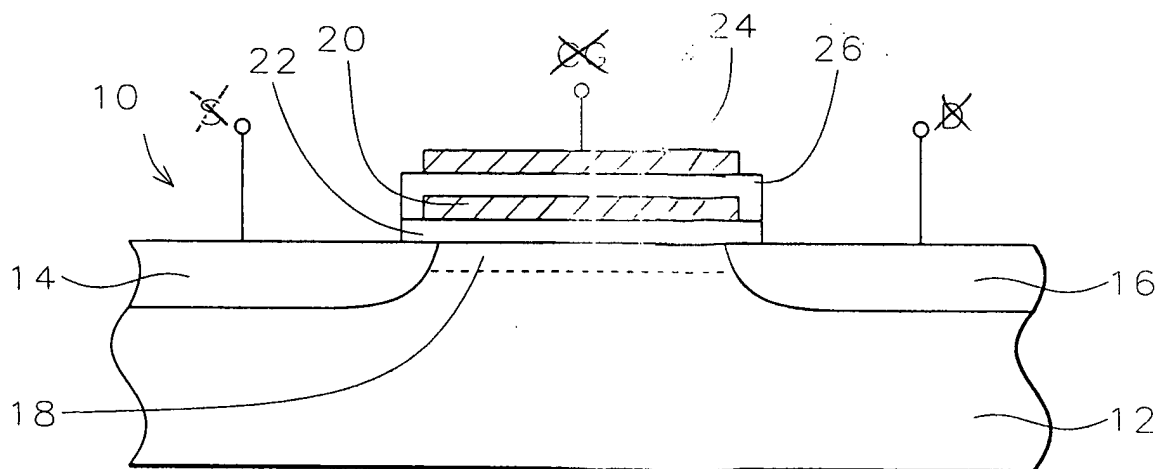


FIG. 1

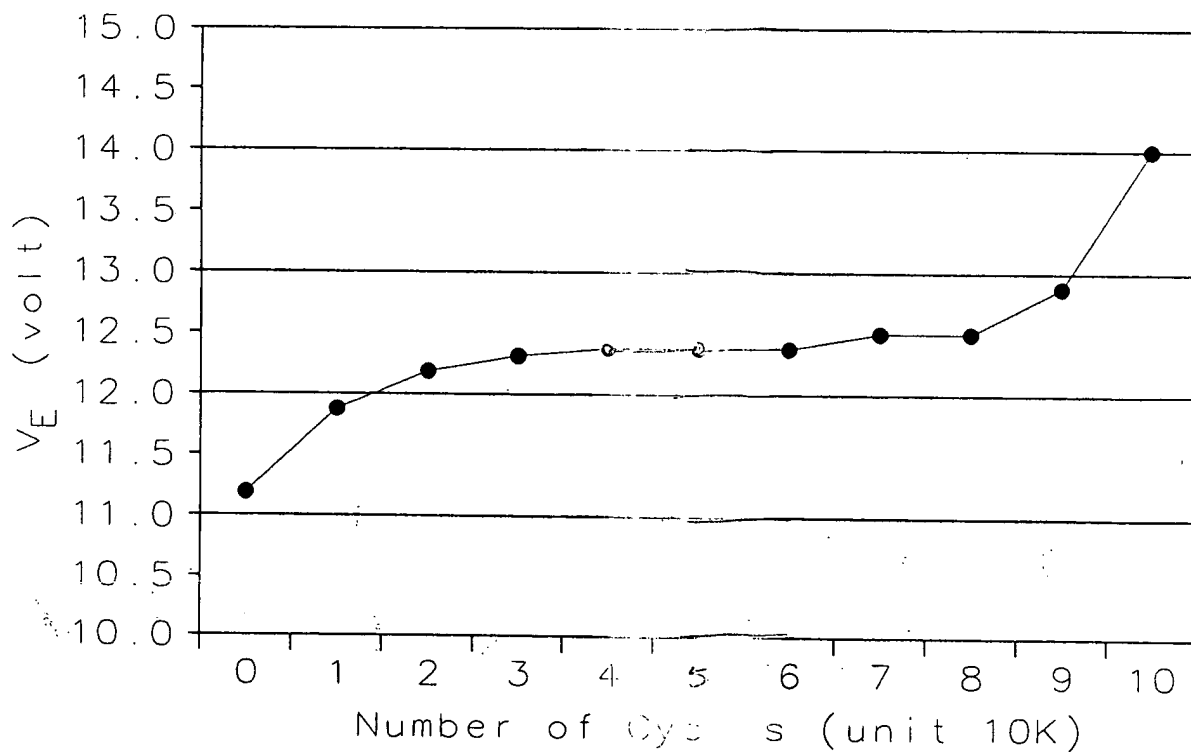


FIG. 2

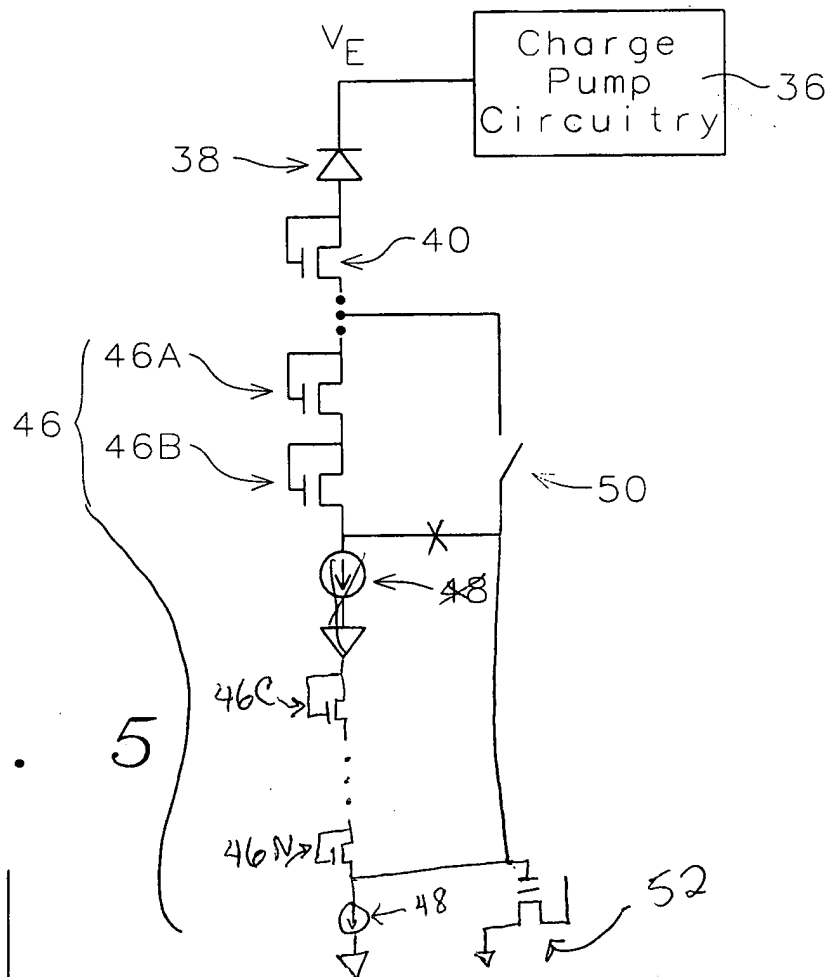
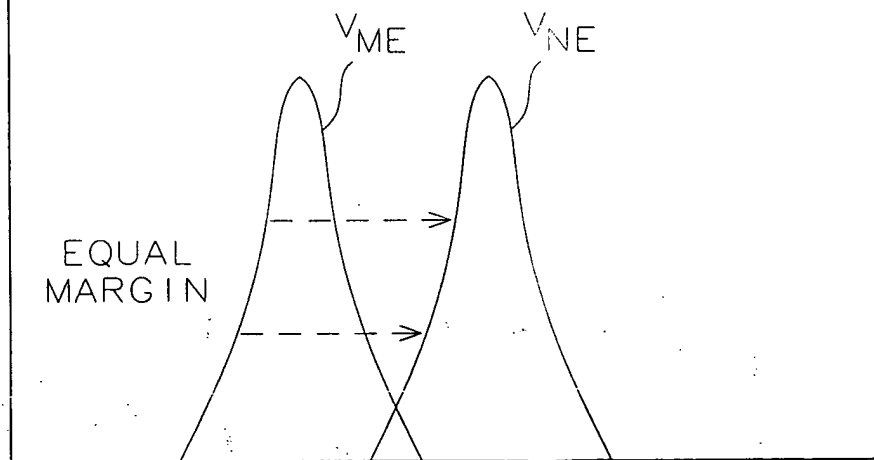


FIG. 5

Number of Samples



Chip Erase Voltage

FIG. 6